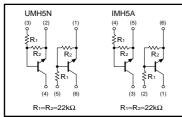
General purpose (dual digital transistors) UMH5N / IMH5A

Features

 Two DTC124E chips in a EMT or UMT or SMT package.

Circuit schematic



• Absolute maximum ratings (Ta = 25°C)

Parame	ter	Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		Vin	40	v	
input voltage		VIN	-10	v	
Output current		lo	30	mA	
Collector current		IC(MAX)	100	mA	
Power dissipation	UMH5N	Pd	150(TOTAL)	mW *1 *2	
	IMH5A	Fu	300(TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	
*1 120mW por element mu	at not he aven	a da d			

*1 120mW per element must not be exceeded *2 200mW per element must not be exceeded

•Package, marking, and packaging specifications

Туре	UMH5N	IMH5A
Package	UMT6	SMT6
Marking	H5	H5
Code	TR	T108
Basic ordering unit (pieces)	3000	3000

•Dimensions (Unit : mm) UMH5N 0.9 2.0 , <u>1.3</u> 0.7 (6) (5) (4) Ŕ 25 888 0.1Min. (1) (2) (3) 0.2 0.15 ROHM : UMT6 EIAJ : SC-88 Each lead has same dimensions IMH5A 2.9 1.9 0.8 (5) (6) 2.8 (3) (2) (1) 0.3 ROHM : SMT6 EIAJ : SC-74 Each lead has same dimensions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI (off)	-	-	0.5	V	Vcc=5V, Io=100µA
	VI (on)	3	-	-		Vo=0.2V, Io=5mA
Output voltage	VO (on)	-	0.1	0.3	V	lo=10mA, li=0.5mA
Input current	h	-	-	0.36	mA	Vi=5V
Output current	IO (off)	-	-	0.5	μΑ	Vcc=50V, VI=0V
DC current gain	Gi	56	-	-	-	Vo=5V, Io=5mA
Transition frequency	fτ	-	250	-	MHz	Vce=10V, Ie= -5mA , f=100MHz *
Input resistance	R1	15.4	22	28.6	kΩ	_
Resistance ratio	R2/R1	0.8	1	1.2	-	-

* Characteristics of built-in transistor



Transistors

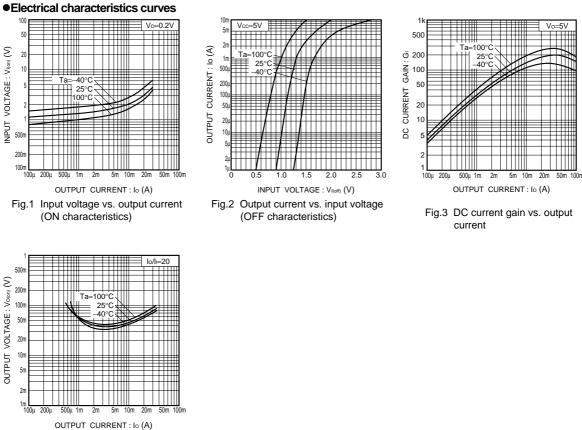


Fig.4 Output voltage vs. output current

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